

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,759,707 B2
DATED : July 6, 2004
INVENTOR(S) : Prall

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 10.

Line 48, "18. The memory device according to claim 16, the" should read -- 18. The memory device according to claim 16, wherein the --;

Column 11.

Line 12, "the memory the device defines a square feature size of" should read -- the memory device defines a square feature size of --;

Column 12.

Line 33, "ing gate and memory device has a square feature size" should read -- ing gate and the memory device has a square feature size --;

Line 35, "35. The memory device of claim 34, the drain comprises" should read -- 35. The memory device of claim 34, wherein the drain comprises --;

Line 50, "42. The memory device wherein the wordlines are above" should read -- 42. The memory device of claim 41, wherein the wordlines are above --; and

Line 58, "46. The memory device of claim 45, further comprising a" should read -- 46. The memory device of claim 45 further comprising a --.

Signed and Sealed this

Twenty-fifth Day of January, 2005



JON W. DUDAS
Director of the United States Patent and Trademark Office